	Application No.	Applicant(s)
Notice of Allowability	10/517,772	FUKUDA ET AL.
	Examiner	Art Unit
	 Walter L. Lindsay, Jr.	2812
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with the (OR REMAINS) CLOSED in this a or other appropriate communication is subject 3 and MPEP 1308.	pplication. If not included on will be mailed in due course. THIS
2. X The allowed claim(s) is/are <u>1-44</u> .	·	
3.		
 Attachment(s) 1. ⋈ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ⋈ Information Disclosure Statements (PTO/SB/08),	5. ☐ Notice of Informal 6. ☐ Interview Summar Paper No./Mail D 7. ☐ Examiner's Amen 8. ☑ Examiner's Stater 9. ☐ Other	y (PTO-413), ate

Continuation of Attachment(s) 3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date: 8/8/2007, 3/4/2005,12/27/2004.

Application/Control Number: 10/517,772

Art Unit: 2812

DETAILED ACTION

This Office Action is in response to an Application filed on 12/27/2004.

Currently, claims 1-44 are pending.

Allowable Subject Matter

- 1. Claims 1-44 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: the prior art, either singly or in combination fails to anticipate or render obvious, the limitations of:

carbide having a (000-1) face orientation; and wherein the gate insulation layer is formed in an atmosphere containing 1% or more H.sub.2O (water) vapor at a temperature of from 800.degree. C. to 1150.degree. C. to reduce an interface trap density of an interface between the gate insulation layer and the semiconductor region, as required by claim 1;

carbide having a (000-1) face orientation; and wherein the step of forming the gate insulation layer is followed by heat treatment in an atmosphere containing H.sub.2 (hydrogen) gas or H.sub.2O (water) vapor to reduce an interface trap density of an interface between the gate insulation layer and the semiconductor region, as required by claim 3;

... forming a gate insulation layer on a semiconductor region formed of silicon carbide having a (000-1) face orientation; and wherein the step of forming the gate insulation layer is followed by heat treatment in an atmosphere containing H.sub.20

Application/Control Number: 10/517,772

Art Unit: 2812

(water) vapor, followed by heat treatment in an atmosphere containing H.sub.2 (hydrogen) gas to reduce an interface trap density of an interface between the gate insulation layer and the semiconductor region, as required by claim 4;

...gate insulation layer on a semiconductor region of (000-1) face silicon carbide, a gate electrode on the gate insulation layer and an electrode on the semiconductor region, wherein a hydrogen or hydroxyl group (OH) level in the gate insulation layer is from 1E19/cm.sup.3 to 1E20/cm.sup.3, as required by claim 27; and

...a gate insulation layer on a semiconductor region of (000-1) face silicon carbide, a gate electrode on the gate insulation layer and an electrode on the semiconductor region, wherein a hydrogen or hydroxyl group (OH) level at an interface between the gate insulation layer and the semiconductor region is within a range of from 1E20/cm.sup.3 to 1E22/cm.sup.3, as required by claim 36.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Walter L. Lindsay, Jr. whose telephone number is (571) 272-1674. The examiner can normally be reached on Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael S. Lebentritt can be reached on (571) 272-1873. The fax phone

Art Unit: 2812

number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Walter L. Lindsay, Jr. Primary Examiner Art Unit 2812

September 2 2007